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1.Order the following memory component base on its speed (from slow to fast)

D. L1 casche

A. program counter

B. Rom

C. Ram

E. second memory

2. Group the following memory into the following category.

-Volatile Memory

. DRAM

. SRAM

. . Rom

. SDRAM

. DDR3

. DDR4

-Non-Volatile Memory

. Hard Disk Drives

. Floppy disk

. Optical disk

3. We have a memory schema as below.

1. Describe big-endian the most significant byte of the data in placed at the byte with the lowest address.
2. Describe little endian the least significant byte of the data in placed at the byte with the lowest address.
3. The size of memory (A) has 13 chip

-the size of memory(B) has 4 chip and 32 bite

-the size of memory(C) has 4 chip and 32 bite